

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled "A PLANARIZED SEMICONDUCTOR INTERCONNECT TOPOGRAPHY AND METHOD FOR POLISHING A METAL LAYER TO FORM INTERCONNECT," the specification of which:

- ☒ is attached hereto.
☐ was filed on _____ as Application Serial No. _____
and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in 37 C.F.R. § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate listed below, or under § 365(a) of any PCT international application listed below designating least one country other than the United States of America, and have identified below any foreign application for patent or inventor's certificate, or of any PCT international application, having a filing date before that of the application on which priority is claimed.

<u>Prior Foreign Application No.</u>	<u>Country</u>	<u>Filing Date</u> <u>(mm/dd/yy)</u>	<u>Priority</u> <u>Claimed</u>	<u>Cert. copy</u> <u>Attached</u>
N/A				

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below.

<u>Provisional Application No.</u>	<u>Filing Date</u> <u>(mm/dd/yy)</u>
N/A	

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below, or under § 365(c) of any PCT international application listed below designating the United States of America, and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in 37 C.F.R. § 1.56, which became available between the filing date of the prior application and the national or PCT international filing date of this application.

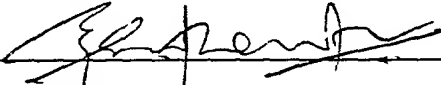
<u>Parent Application No.</u>	<u>Filing Date</u> <u>(mm/dd/yy)</u>	<u>Parent Patent No. (if applicable) or Status</u>
N/A		

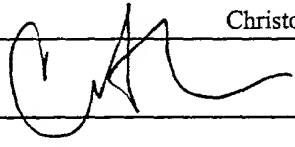
The Assignee hereby revokes any previous Powers of Attorney and appoints Andrew D. Fortney, Reg. No. 34,600 of Cypress Semiconductor Corporation, and Kevin L. Daffer, Reg. No. 34,146; B. Noel Kivlin, Reg. No. 33,929; and, Eric B. Meyertons, Reg. No. 34,876 of the firm of Conley, Rose & Tayon, as attorney or agent for so long as they remain with such company or firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive the Letters Patent.

Please direct all communications to:

Kevin L. Daffer
Conley, Rose & Tayon, P.C.
P.O. Box 398
Austin, Texas 78767-0398
Phone: (512) 476-1400

I hereby declare that all statements made herein of my own knowledge are true and that all statements made herein on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Inventor's Full Name: Anantha R. Sethuraman
Inventor's Signature:  Date: August 21, 1998
City and State (or Foreign Country) of Residence: Fremont, CA Citizenship: India
Post Office and Residence Address: 34152 Finnigan Terrace, Fremont, CA 94555
(Include number, street name, city, state and zip code)

Inventor's Full Name: Christopher A. Seams
Inventor's Signature:  Date: 8/21/98
City and State (or Foreign Country) of Residence: Pleasanton, CA Citizenship: U.S.A.
Post Office and Residence Address: 562 Trebbian Place, Pleasanton, CA 94566
(Include number, street name, city, state and zip code)



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UNITED STATES DEPARTMENT OF COMMERCE
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JANUARY 04, 1999

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UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 08/31/1998

REEL/FRAME: 9435/0506
NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:
SETHURAMAN, ANANTHA R.

DOC DATE: 08/21/1998

ASSIGNOR:
SEAMS, CHRISTOPHER A.

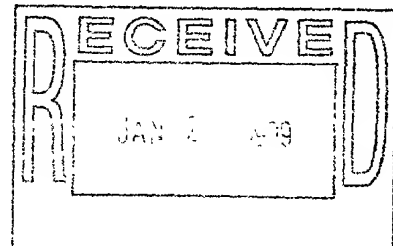
DOC DATE: 08/21/1998

ASSIGNEE:
CYPRESS SEMICONDUCTOR CORP.
3901 N. 1ST STREET
SAN JOSE, CALIFORNIA 95134

SERIAL NUMBER: 09143723
PATENT NUMBER:

FILING DATE: 08/31/1998
ISSUE DATE:

LAWAN FLETCHER, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS



09-14-1998

Atty Dkt. 5298-02500

To the Honorable Commissioner of Patents and Trademarks

100827846

copy thereof.

1. Name of conveying party(ies):

ANANTHA R. SETHURAMAN
CHRISTOPHER A. SEAMS

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: Cypress Semiconductor Corp.

Internal Address:

Street Address: 3901 N. 1st Street

City San Jose State CA ZIP 95134

Additional name(s) & address(es) attached? ☐ Yes ☐ No

3. Nature of Conveyance:

- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other

Execution Date: August 21, 1998

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of this application is: August 31, 1998

A. Patent Application No.(s) B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Kevin L. Daffer

Internal Address: Conley, Rose & Tayon, P.C.

Street Address: P.O. Box 398

City Austin State TX ZIP 78767-0398

6. Total number of applications and patents involved: 1

7. Total fee (37 CFR 3.41):\$ 40.00

- ☒ Enclosed
☐ Authorized to be charged to deposit account

8. Deposit account number:

(Attach a duplicate copy of this page if paying by deposit account)

09/08/1998 BUTLER 00000054 09143723

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9. Statement and Signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Kevin L. Daffer

Name of Person Signing
Reg. No. 34,146

Signature

August 31, 1998

Date

Total number of pages comprising cover sheet: 3

ASSIGNMENT

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned, does hereby:

SELL, ASSIGN AND TRANSFER to CYPRESS SEMICONDUCTOR CORPORATION (the "Assignee"), having a place of business at 3939 North First Street, San Jose, CA, 95134 the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned concurrently herewith, and is entitled "A PLANARIZED SEMICONDUCTOR INTERCONNECT TOPOGRAPHY AND METHOD FOR POLISHING A METAL LAYER TO FORM INTERCONNECT", such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

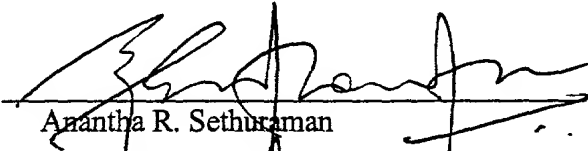
WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee; and

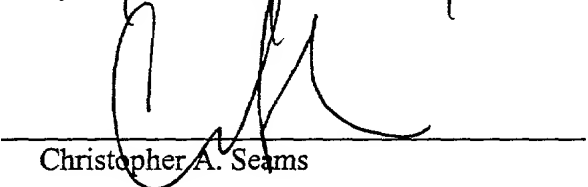
COVENANT AND AGREE that the above is and will be binding on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

FURTHER COVENANT that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

(Signature) 
Anantha R. Sethuraman

Date: August 21, 1998

(Signature) 
Christopher A. Seams

Date: 8/21/98

Please direct all communication as follows:

Kevin L. Daffer
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